



LENDI INSTITUTE OF ENGINEERING AND TECHNOLOGY

Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

List of Projects (Academic Year: 2019-20)

S.No	Project Title	Domain	Classification	Relevant to POs & PSOs
1	Design of Low PDP Ternary Circuits Utilizing Carbon Nano Tube Field Effect Transistors	VLSI Design	Research	PO1-PO12, PSO1, PSO2
2	Design of IoT Based Smart Sensor Home	IoT	Application	PO1-PO12, PSO1, PSO2
3	Design of Parity Generator Circuits Using QCA	VLSI Design	Research	PO1-PO12, PSO1, PSO2
4	Design of Accident Identification with Automatic Ambulance Rescue System	Embedded systems	Application	PO1-PO12, PSO1, PSO2
5	Method of Visual and Infrared Image Fusion for Object Detection	Image processing	Research	PO1-PO12, PSO1, PSO2
6	Implementation of Speed Optimized Reversible Logic Basic Shift Register Design	Image processing	Research	PO1-PO12, PSO1, PSO2
7	An Implementation of Deblurring of An Image Using Hierarchical Approach	Image processing	Research	PO1-PO12, PSO1, PSO2
8	Design of Low Power Full Adder Using Adiabatic Logic	VLSI Design	Research	PO1-PO12, PSO1, PSO2
9	Design of MIMO Dielectric Resonator Antenna with Improved Isolation for C-Band Applications	Antennas	Research	PO1-PO12, PSO1, PSO2
10	Implementation of Distance Vector and Differential Evolution Node Localization Algorithms by Received Signal Strength Indicator	Communications	Research	PO1-PO12, PSO1, PSO2

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11	Design of Novel Decoder Using Quantum Cellular Automata	VLSI Design	Research	PO1-PO12, PSO1, PSO2
12	Real Time Implementation of Home Automation and Health Monitoring System Using Google Assistant	IoT	Application	PO1-PO12, PSO1, PSO2
13	An Effective Sharp Curve Lane Detector	Image processing	Research	PO1-PO12, PSO1, PSO2
14	Development of Smart Dustbin Monitoring System	IoT	Application	PO1-PO12, PSO1, PSO2
15	Design of Three Layered Broadband Stacked Microstrip Patch Antenna	Antennas	Research	PO1-PO12, PSO1, PSO2
16	Developing Spatial Modulation Technique for the improvement of BER and Spectral Efficiency over MRC and MIMO in 5G Technology	Communications	Research	PO1-PO12, PSO1, PSO2
17	Parametric optimization of low-profile V-band antenna for millimeter-wave applications	Antennas	Research	PO1-PO12, PSO1, PSO2
18	Design of MIMO-OFDM for high-rate Underwater Acoustic Communication	Communications	Research	PO1-PO12, PSO1, PSO2
19	Design of B shaped patch antenna using optimized RIS layer	Antennas	Research	PO1-PO12, PSO1, PSO2
20	Implementation of vector perturbation precoding and Approximate message passing in MU-MIMO downlink system	Communications	Research	PO1-PO12, PSO1, PSO2
21	Design and analysis of digital filters for speech signals using multi-rate signal processing	Signal Processing	Research	PO1-PO12, PSO1, PSO2
22	Implementation of Faster Region-CNN for Image classification using MATLAB	VLSI Design	Research	PO1-PO12, PSO1, PSO2

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23	Design of Smart Drip Irrigation system for Farming	IoT	Application	PO1-PO12, PSO1, PSO2
24	Design of Triband Truncated patch and Circular patch antenna	Antennas	Research	PO1-PO12, PSO1, PSO2
25	Some investigations on wearable antennas for wireless applications	Antennas	Research	PO1-PO12, PSO1, PSO2
26	Development of New Blind Medical Image Watermarking Using Weber Descriptor & Arnold Chaotic Map	Image Processing	Research	PO1-PO12, PSO1, PSO2
27	Development of RaspberryPi Based IoT Agricultural System Using Fuzzy Techniques	IoT	Application	PO1-PO12, PSO1, PSO2
28	VLSI implementation of linear feedback shift register (LFSR) based test pattern generator for pseudo exhaustive testing	Image Processing	Research	PO1-PO12, PSO1, PSO2
29	A Low-Profile Pentagon Inscribed in Circle Shaped Wide Band Antenna Design for C-Band Applications	Antennas	Research	PO1-PO12, PSO1, PSO2
30	An efficient brain tumor cells detection and categorizing using SVM	Image Processing	Research	PO1-PO12, PSO1, PSO2
31	Implementation of 32 bit Carry Look Ahead Adder using GDI design	VLSI Design	Research	PO1-PO12, PSO1, PSO2
32	VLSI implementation of Massive MIMO systems utilizing OFDM guard bands for N-point FFT/IFFT processor	Communications	Research	PO1-PO12, PSO1, PSO2
33	Designing of IFFT-FFT OFDM using different digital modulation techniques	Communications	Research	PO1-PO12, PSO1, PSO2

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34	Implementation of ultra-wideband Microstrip to Microstrip vertical transition	Antennas	Research	PO1-PO12, PSO1, PSO2
35	An implementation for dehazing of underwater image by using multiscale fusion strategy	Image Processing	Research	PO1-PO12, PSO1, PSO2
36	An Efficient VLSI Design of 32x32 Bit Multiplier Using Wallace Tree Algorithm In Vivado HLS and Xilinx ISE Using VHDL	VLSI Design	Research	PO1-PO12, PSO1, PSO2
37	Real Time Text Recognition System for Visually Impaired People	IoT	Application	PO1-PO12, PSO1, PSO2
38	Design of dumbbell shaped MIMO antenna for wearable applications	Antennas	Research	PO1-PO12, PSO1, PSO2
39	Indoor localization accuracy improvement in wireless sensor networks using convolutional neural network	Wireless Sensor Networks	Research	PO1-PO12, PSO1, PSO2
40	Implementation of Reconfigurable Inverted F-Shaped Monopole Antenna for Dual Band Applications	Antennas	Research	PO1-PO12, PSO1, PSO2
41	Design of Multilayer ALU using Landauer in Field Coupled Nano Technology	VLSI Design	Research	PO1-PO12, PSO1, PSO2
42	Design of Hybrid Dielectric Resonator Antenna for Multifunctional Applications	Antennas	Research	PO1-PO12, PSO1, PSO2
43	Implementation of an optimized DFT-SpreadOFDM technique to minimize PAPR Using QPSK Symbols in 5G Technology	Communications	Research	PO1-PO12, PSO1, PSO2

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44	Implementation of Binary Multiplier Using Vedic Mathematics	Image Processing	Research	PO1-PO12, PSO1, PSO2


Project Coordinator


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